

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	426	345/205,206.ccls.	US-PGPU B; USPAT	OR	OFF	2005/03/02 15:28
L2	338	l1 and @ad<="20010328"	US-PGPU B; USPAT	OR	OFF	2005/03/02 15:34
L3	1	l2 and (recess same block)	US-PGPU B; USPAT	OR	OFF	2005/03/02 16:08
L4	4	l2 and (recess same substrate)	US-PGPU B; USPAT	OR	OFF	2005/03/02 16:08
L5	11	l2 and recess	US-PGPU B; USPAT	OR	OFF	2005/03/02 15:34
L6	175	345/1.3.ccls.	US-PGPU B; USPAT	OR	OFF	2005/03/02 15:34
L7	136	l6 and @ad<="20010328"	US-PGPU B; USPAT	OR	OFF	2005/03/02 16:19
L8	4	l7 and recess	US-PGPU B; USPAT	OR	OFF	2005/03/02 15:40
L9	1204	"345"/\$.ccls. and recess	US-PGPU B; USPAT	OR	OFF	2005/03/02 15:40
L10	52	"345"/\$.ccls. and (recess same block)	US-PGPU B; USPAT	OR	OFF	2005/03/02 15:41
L11	9347	recess same block	EPO; JPO; DERWENT	OR	OFF	2005/03/02 15:45
L12	5	l11 and display and substrate	EPO; JPO; DERWENT	OR	OFF	2005/03/02 15:46
L13	1125	(recess same substrate) and display	EPO; JPO; DERWENT	OR	ON	2005/03/02 15:47
L14	556	recess same substrate same display	EPO; JPO; DERWENT	OR	ON	2005/03/02 15:47

L15	6	I14 and integrated adj3 circuit	EPO; JPO; DERWENT	OR	ON	2005/03/02 15:49
L16	25	seligson-d\$.in.	US-PGPU B; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/02 15:59
L17	1	I16 and recess	US-PGPU B; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/02 15:50
L19	3827	345/1.1,1.2,44-46,55,76, 87.ccls.	US-PGPU B; USPAT	OR	OFF	2005/03/02 16:07
L20	5	I19 and (recess same block)	US-PGPU B; USPAT	OR	OFF	2005/03/02 16:07
L21	18342	"349"/\$.ccls.	US-PGPU B; USPAT	OR	OFF	2005/03/02 16:07
L22	234	I21 and (recess same substrate)	US-PGPU B; USPAT	OR	OFF	2005/03/02 16:08
L23	16	I21 and (recess same block)	US-PGPU B; USPAT	OR	OFF	2005/03/02 16:11
L24	272	345/205.ccls.	US-PGPU B; USPAT	OR	OFF	2005/03/02 16:19
L25	201	345/206.ccls.	US-PGPU B; USPAT	OR	OFF	2005/03/02 16:24
L26	169	I25 and @ad<="20010328"	US-PGPU B; USPAT	OR	OFF	2005/03/02 16:19

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	659	345/82,83.ccls.	US-PGPU B; USPAT	OR	OFF	2005/03/02 16:57
L2	0	I1 and (recess same block)	US-PGPU B; USPAT	OR	OFF	2005/03/02 16:58
L3	5	I1 and (recess same substrate)	US-PGPU B; USPAT	OR	ON	2005/03/02 16:58